

Notice of References Cited	Application/Control No. 10/757,227		Applicant(s)/Patent Under Reexamination DEWITT ET AL.	
	Examiner Arpan P. Savla		Art Unit 2185	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,710,881	01-1998	Gupta et al.	709/200
*	B	US-5,822,763	10-1998	Baylor et al.	711/141
*	C	US-5,928,334	07-1999	Mandyam et al.	709/248
*	D	US-6,094,709	07-2000	Baylor et al.	711/141
*	E	US-6,285,974	09-2001	Mandyam et al.	703/13
*	F	US-6,636,950	10-2003	Mithal et al.	711/141
*	G	US-2004/0205302	10-2004	Cantrill, Bryan	711/141
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Torrellas et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
	V	Rothman et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.